

Fig. 1

FIG. 2 is a block diagram of a system 250 for interfacing a device 252 to a selected interface 257. The system 250 includes a test interface 252, a programming interface 254, and an operation interface 256. The test interface 252 is connected to the programming interface 254 via a bidirectional bus 253. The programming interface 254 is connected to the operation interface 256 via a bidirectional bus 257. The test interface 252 is also connected to the operation interface 256 via a bidirectional bus 255. The system 250 is controlled by a select signal 251 and a selected interface signal 257.

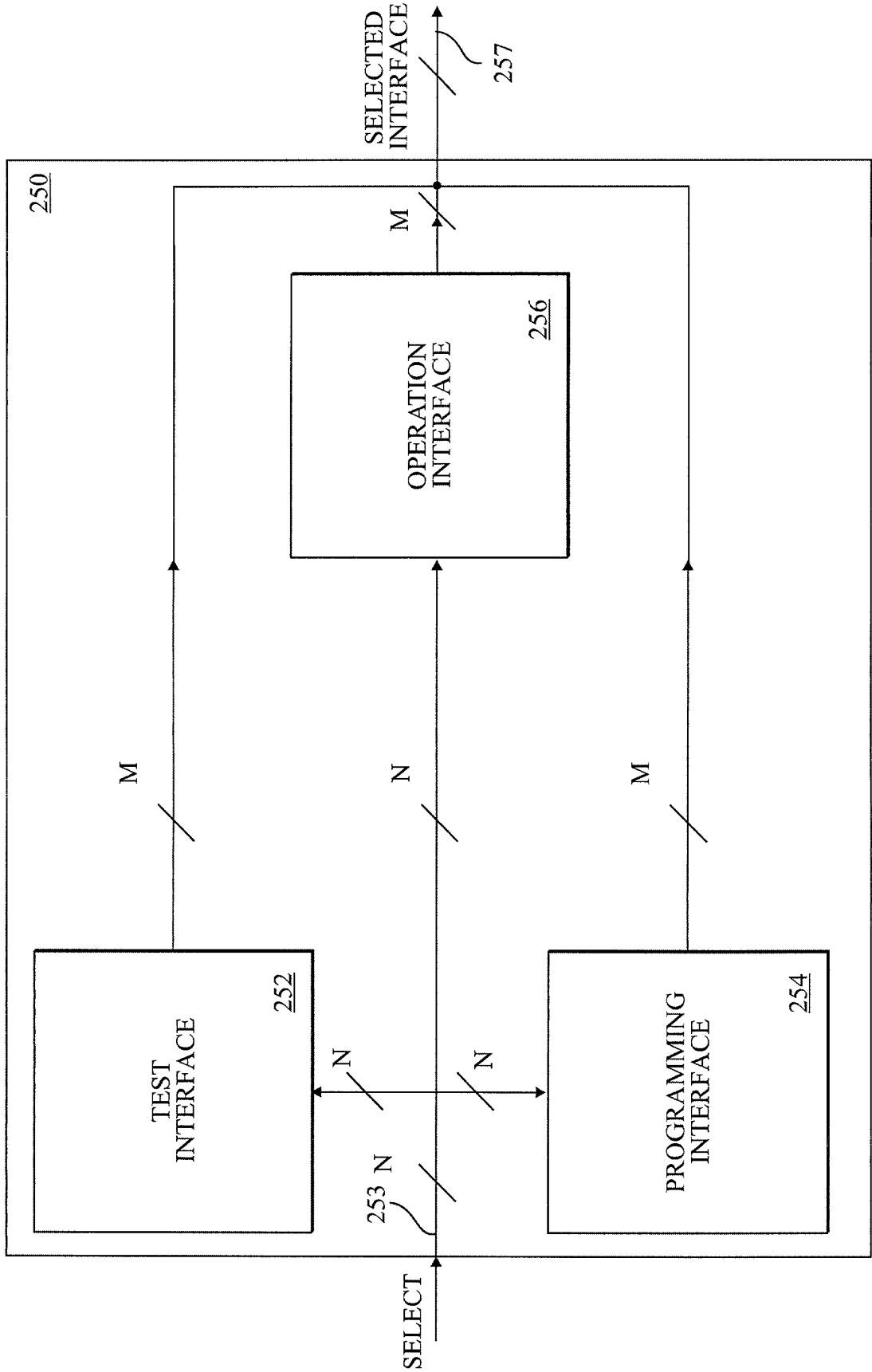
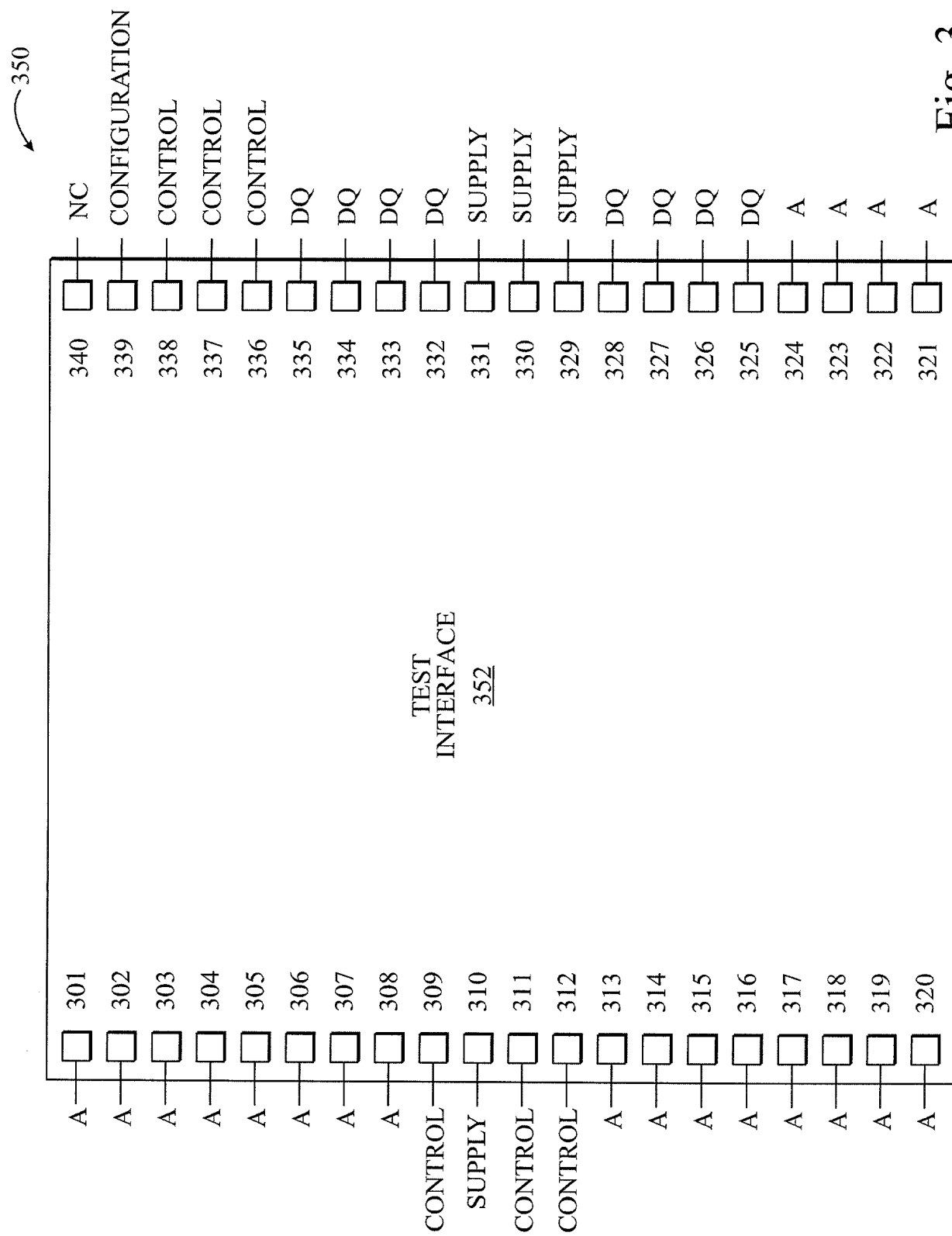


Fig. 2



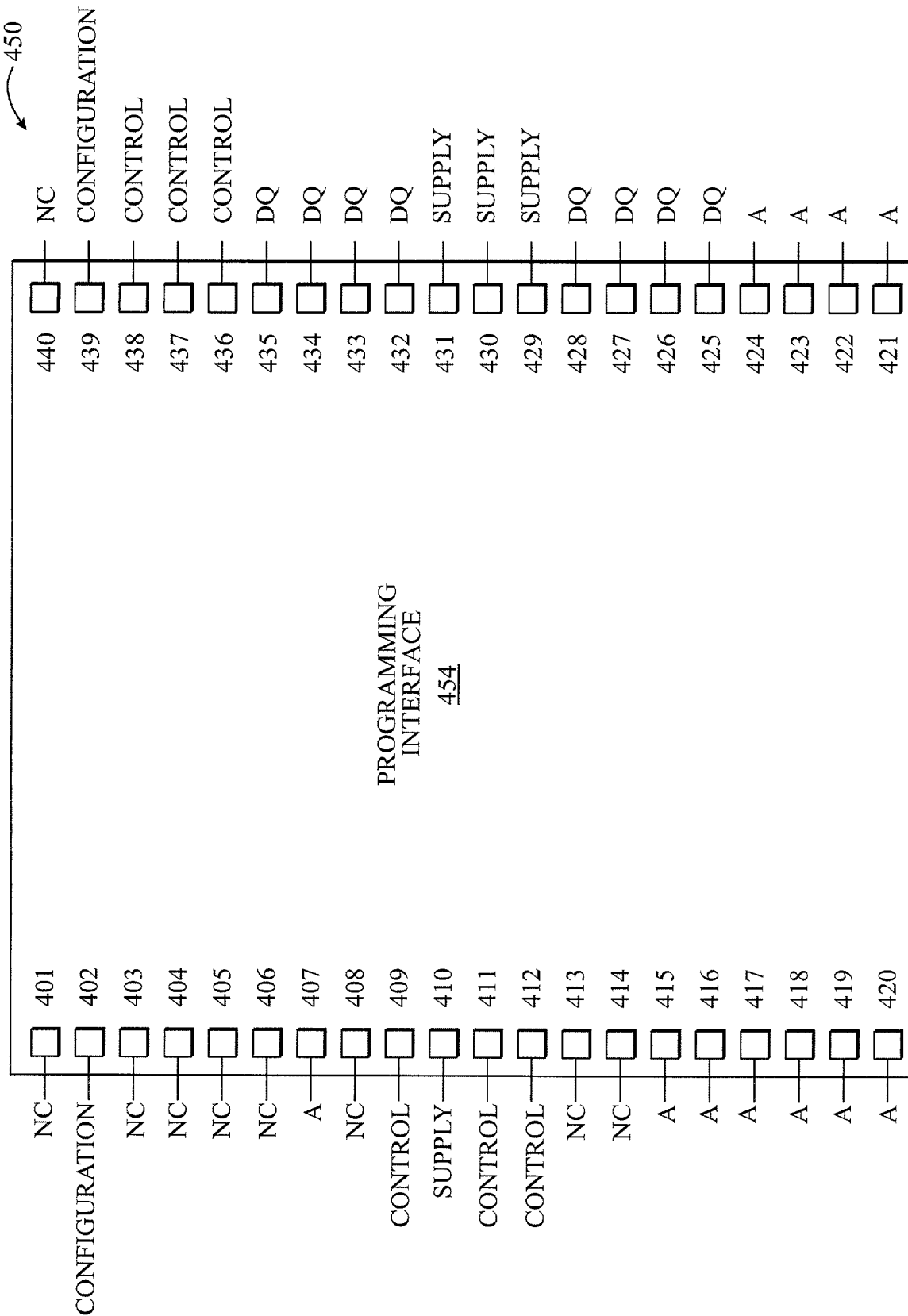


Fig. 4

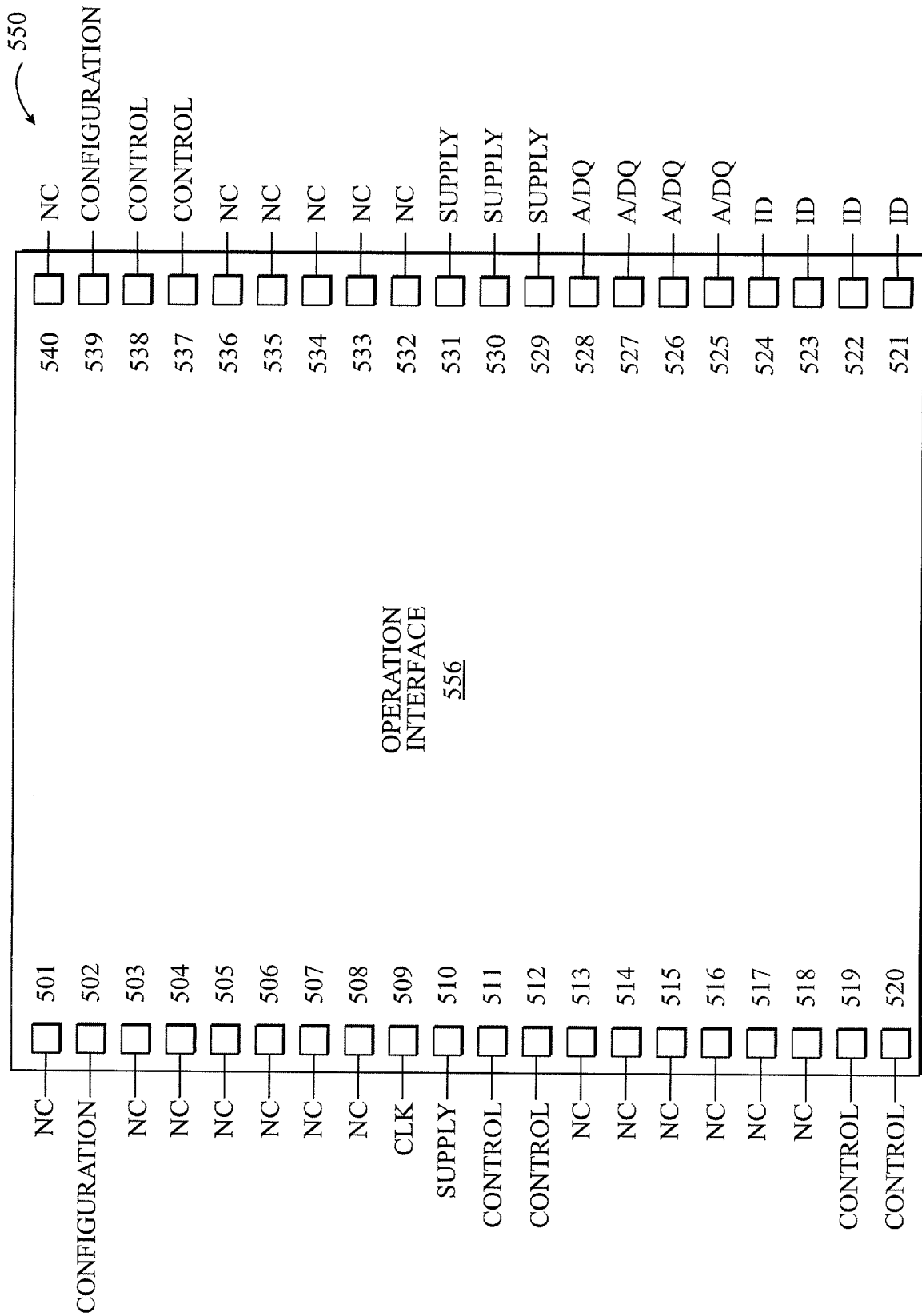


Fig. 5

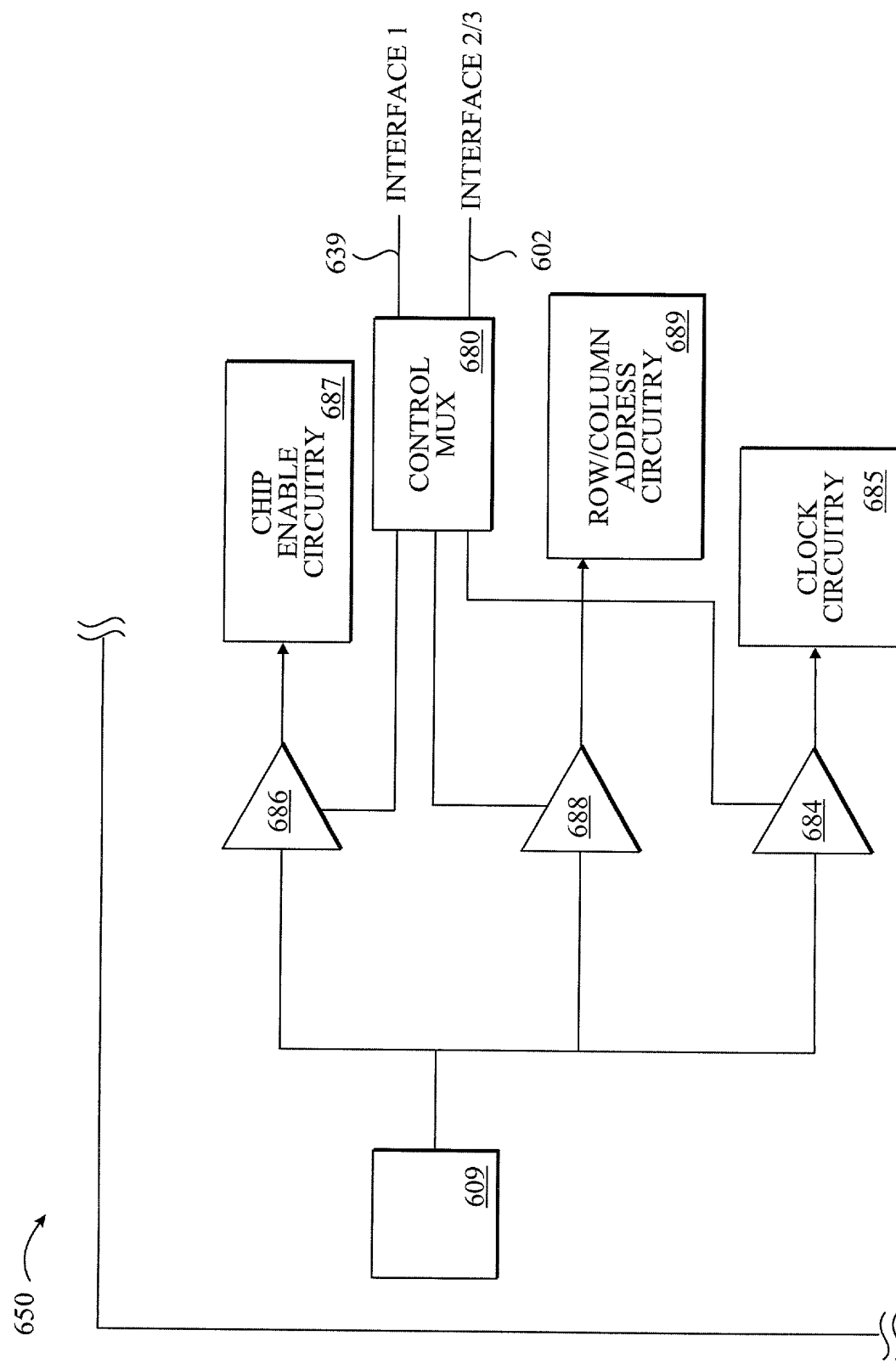


Fig. 6